



Attorney Docket SBL 126

#201
Donald E
G. Foul
5/16/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)

Yamazaki et al.)

Serial No.: 09/262,657)

Filed: March 4, 1999)

For: Semiconductor Device And)
Method For Manufacturing)
The Same)

Art Unit: 2822)

Examiner: M. Prenty)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 9, 2003

Shannon Wallace

Shannon Wallace 5/9/03
Signature Date

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TECHNOLOGY CENTER 2800

AMENDMENT E

Sir:

In response to the Office Action dated January 9, 2003, a one month extension of time being separately requested, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend the claims as follows:

1. (Third Amendment) A semiconductor device comprising: 05/19/2003 AFORD1 00000002 501039 09262657

a substrate;

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72.00 CH

a first thin film transistor having a first active layer comprising $\text{Si}_{1-X}\text{Ge}_X$ where $0 < X < 1$

formed over said substrate; and